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Robin E. Barnum (Depositor's name)  
*[Signature]* (Signature)  
March 22, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/990,841	11/09/2001	Pochiraju Srinivas Rao	TI-31068	2705

TITLE OF INVENTION: SUB-BANDING OF DISPLAY LIST AND VIDEO BUFFER FOR PAGE RENDERING IN A DIGITAL SIGNAL PROCESSOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/01/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
QIN, YIXING	2622	358-001160

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  
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1 Robert D. Marshall, Jr.  
2 W. James Brady, III  
3 Frederick J. Telecky, Jr.

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Texas Instruments Incorporated

Dallas, TX USA

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

*Robert D. Marshall, Jr.*

Date March 22, 2006

Typed or printed name

Robert D. Marshall, Jr.

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

TI-31068

Pochiraju Srinivas Rao

Art Unit: 2622

Serial No.: 09/990,841

Examiner: Yixing Qin


Filed: November 9, 2001

Conf. No.: 2705

For: Sub-Banding of Display List and Video Buffer for Page Rendering in a Digital Signal Processor

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-2885 on the date shown below:

  
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<b>NAME OF INVENTOR(S):</b> Pochiraju Srinivas Rao	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> Serial No.: <b>09/990,841</b> Filing Date: November 9, 2001	
<b>TITLE OF INVENTION:</b> Sub-Banding of Display List and Video Buffer for Page Rendering in a Digital Signal Processor	
<b>TI FILE NO.:</b> <b>TI-31068</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED:</b> 3/22/06 <b>DUE:</b> 6/1/06 <b>ATTY/SECY:</b> RDM/reb	

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